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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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First Named Inventor

Charles Jay Alpert et al

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Group Art Unit

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Examiner Name

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### U.S. PATENT DOCUMENTS

Examiner Initials	IDS Doc #	U.S. Patent Document Number	Name of Patentee or Applicant	Date of Publication MM-DD-YYYY

### FOREIGN PATENT DOCUMENTS

Examiner Initials	IDS Doc #	Office	Foreign Patent Document Number	Name of Patentee or Applicant	Date of Publication MM-DD-YYYY	English Translation

### OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	IDS Doc #	Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	English Translation
MEH	AA	B. TUTUIANU, F. DARTU, and L. PILEGGI; "An Explicit RC-Circuit Delay Approximation Based on the First Three Moments of the Impulse Response"; <i>IEEE/ACM Design Automation Conference</i> ; 1996; pp. 611-616; Las Vegas, NV, USA	
MEH	AB	P.R. O'BRIEN, and T.L. SAVARINO; "Modeling the Driving-Point Characteristic of Resistive Interconnect for Accurate Delay Estimation"; <i>IEEE</i> , 1989; pp. 512-515.	
MEH	AC	J. QIAN, S. PULLELA, and L. PILLAGE; "Modeling the 'Effective Capacitance' for the RC Interconnect of CMOS Gates"; <i>IEEE Transaction on Computer-Aided Design of Integrated Circuits and Systems</i> , 1994, pp. 1526-1535, Vol. 13, No. 12.	
MEH	AD	R. KAY, and L. PILEGGI; "PRIMO: Probability Interpretation of Moments for Delay Calculation"; <i>Carnegie Mellon University, Dept. of ECE, Pittsburgh PA.</i> , pp. 1-20.	
MEH	AE	L.T. PILLAGE, and R.A. ROHRER; "Asymptotic Waveform Evaluation for Timing Analysis"; <i>IEEE Transaction on Computer-Aided Design</i> , 1990, pp. 352-366, Vol. 9, No. 4.	
MEH	AF	A.B. KAHNG, and S. MUDDU; "An Analytical Delay Model for RLC Interconnects"; <i>IEEE Transaction on Computer-Aided Design of Integrated Circuits and Systems</i> ; 1997, pp. 1507-1514, Vol. 16, No. 12.	
MEH	AG	C.J. ALPERT, A. DEVGAN, and C. KASHYAP; "A Two Moment RC Delay Metric for Performance Optimization"; 2000.	
MEH	AH	W.C. ELMORE; "The Transient Response of Damped Linear Networks with Particular Regard to Wideband Amplifiers"; <i>University of California, Los Alamos Scientific Laboratory, Santa Fe, New Mexico</i> ; 1948, pp. 55-63, Vol. 19, January 1948.	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
MEH	AI	L. PILEGGI; "Timing Metrics for Physical Design of Deep Submicron Technologies"; Department of Electrical and Computer Engineering, Carnegie Mellon University, Monterey, CA, 1998, pp. 28-33	
MEH	AJ	J. RUBINSTEIN, P. PENFIELD, JR., and M.A. HOROWITZ; "Signal Delay in RC Tree Networks"; IEEE, 1983, pp. 202-210.	
MEH	AK	R. GUPTA, B. KRAUTER, B. TUTUANU, J. WILLIS and L.T. PILEGGI; "The Elmore Delay as a Bound for RC Trees with Generalized Input Signals"; The University of Texas at Austin, Department of Electrical and Computer Engineering, Austin, Texas; 32 <sup>nd</sup> Design Automation Conference, 1995, pp. 364-369.	
MIL	AL	T. LIN, E. ACAR, and L. PILEGGI; "H-GAMMA: An RC Delay Metric Based on a Gamma Distribution Approximation of the Homogeneous Response"; pp. 1-8.	

Examiner Signature		Date Considered	08/12, 2004
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